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The Gazette of the Democratic Socialist Republic of Sri Lanka  
EXTRAORDINARY

අංක 2083/35 - 2018 අගෝස්තු මස 10 වැනි සිකුරාදා - 2018.08.10

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(Published by Authority)

**PART I : SECTION (I) — GENERAL**  
**Government Notifications**

L.D.B. 21/78(x)

**THE UNIVERSITIES ACT, No. 16 OF 1978**

**Order under Section 25A**

BY virtue of the powers vested in me by Section 25A of the Universities Act, No. 16 of 1978, I, Dr. Wijayadasa Rajapakse, the Minister of Higher Education and Cultural Affairs, having obtained a report under Section 70C of the aforesaid Act, in respect of the SLT Campus (Pvt) Ltd., a company incorporated in Sri Lanka under the Companies Act, No. 7 of 2007 and bearing Registration Number PV 101137, do by this Order, amend the Schedule to the Order made under the said Act, and published in *Gazette Extraordinary* No. 1995/39 of December 02, 2016 by the addition immediately after the entry "Bachelor of Science Honours in Engineering in Electronics and Engineering Management" appearing in Column I and its corresponding entry in Column II thereof, of the entries specified under Column I and II respectively of the Schedule II hereto, subject to the conditions specified in Schedule I hereto :-

Dr. WIJAYADASA RAJAPAKSHE,  
Minister of Higher Education and Cultural Affairs.

Colombo,  
08th August, 2018.

**SCHEDULE I**

**CONDITIONS**

SLT Campus (Pvt) Ltd shall, when admitting students for the degree programme specified in Schedule II hereto in respect of the Bachelor of Technology Honours in Electronics, adhere to the minimum entry qualifications of three (03) "S" passes in Physical Science stream or Biological Science stream or Technology stream in one and the same sitting at the GCE Advanced Level Examination or equivalent.

**SCHEDULE II**

<i>Column I</i> <i>Degree</i>	<i>Column II</i> <i>Abbreviations for Degree</i>
Bachelor of Technology Honours in Electronics	B Tec (Hons.) Electronics

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